|  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- |
| **ATTRIBUTES** | | | | | | |
| **DN** | **Name** | **Access Property** | **Size** | **Type1 # Ind.2 # Sel.3** | **MDV4** | **Description** |
| 0Ah | bRefClkFreq | Read / Persistent | 1 byte | D | 01h | Reference Clock Frequency value 0h:19.2 MHz 1h: 26 MHz 2h: 38.4 MHz 3h: Obsolete Others: Reserved |
| NOTE 1:  The type “D” identifies a device level attribute, while the type “A” identifies an array of attributes. If Type = “D”, the attribute is addressed setting INDEX = 00h and SELECTOR = 00h.  NOTE 2:  For array of attributes, “# Ind.” specifies the amount of valid values for the INDEX field in QUERY REQUEST/RESPONSE UPIU. If # Ind =0, the attribute is addressed setting INDEX = 00h.  NOTE 3:  For array of attributes, “# Sel.” specifies the amount of valid values for the SELECTOR field in QUERY REQUEST/RESPONSE UPIU. If #Sel = 0, the attribute is addressed setting SELECTOR = 00h.  NOTE 4:  The column “MDV” (Manufacturer Default Value) specifies attribute values after device manufacturing. | | | | | | |

**Extract from JESD220 JEDEC STANDARD Universal Flash Storage (UFS)**

**Design:**

bRefClkFreq attribute indicates to the device the frequency of the REF\_CLK signal, and its default value corresponds to 26 MHz as per the MDV value of the UFS card, and hence different UFS host systems may set to different value based on their system environment.

This require EDKII UFS pass through driver (MdeModulePkg/Bus/Ufs/UfsPassThruDxe) to receive the input from EDKII\_UFS\_HC\_PLATFORM\_PROTOCOL/ gEdkiiUfsHcPlatformProtocolGuid before it checks and programs. Also, this need to be done before EDKII\_UFS\_HC\_PLATFORM\_PROTOCOL switches to higher gears.